

Figura 1 – Esquemático Amplificador CMOS.

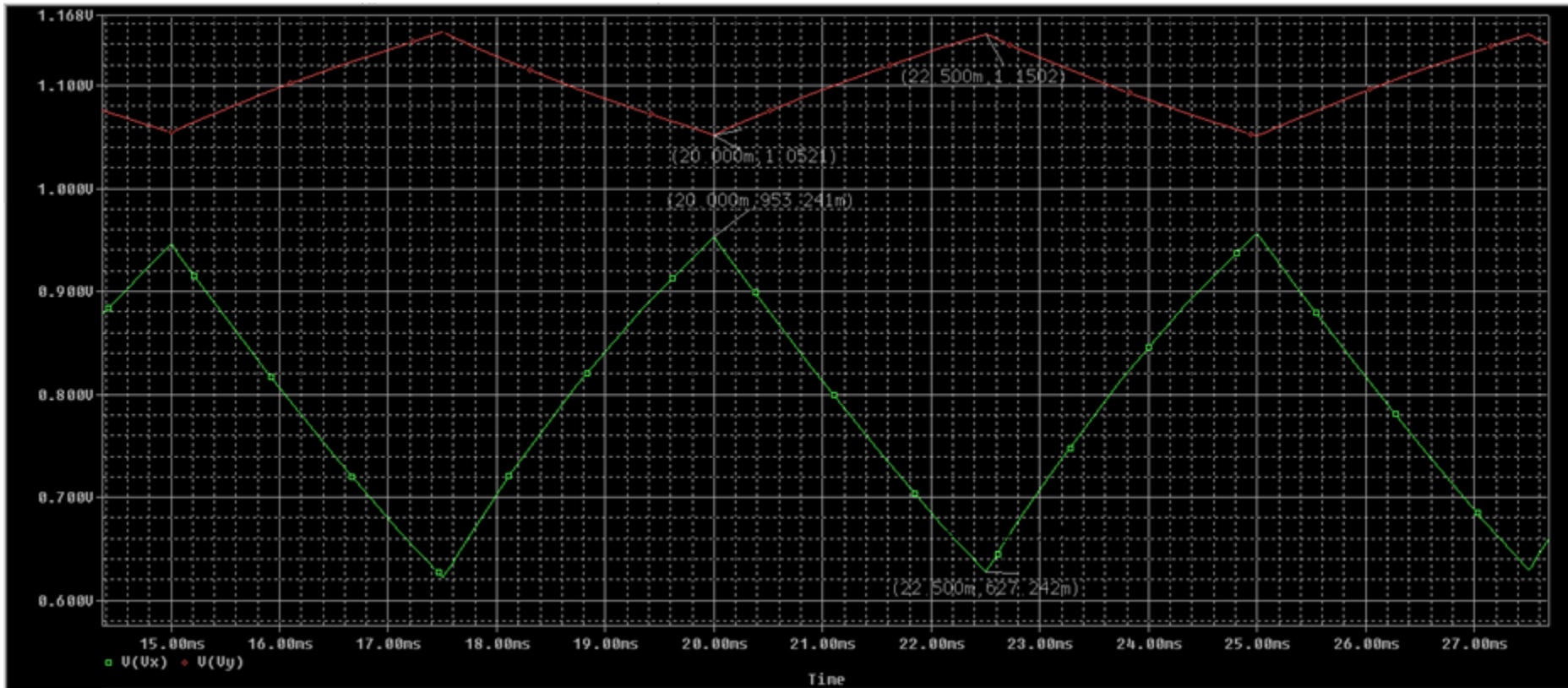


Figura 2 - V_x e V_y para V_{in} de -0.05V a 0.05V.

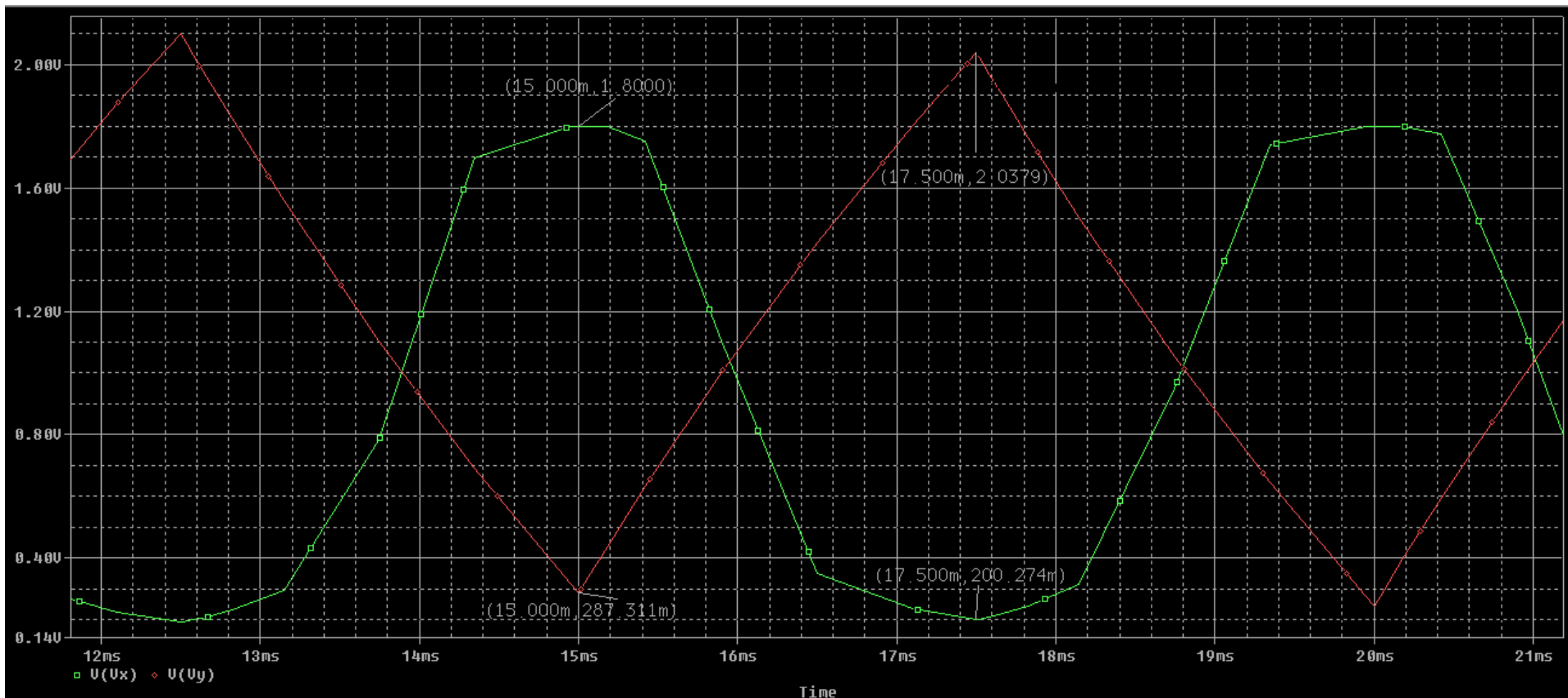


Figura 3 – V_x e V_y para V_{in} de -0.9 a 0.9